

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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APR 06 2004

In re Application of:  
Ding, et al.

Serial No.: 10/016,255

Confirmation No.: 7901

Filed: December 10, 2001

For: Improved Barrier Layer for  
Electroplating Processes

Group Art Unit: 1753

Examiner: Rodney G. McDonald

OFFICIAL

MAIL STOP AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

## RESPONSE TO FINAL OFFICE ACTION DATED FEBRUARY 6, 2004

In response to the Final Office Action dated February 6, 2004, having a shortened statutory period for response set to expire on May 6, 2004, please enter this response and reconsider the claims pending in the application for reasons discussed below. Although Applicant believes that no additional fees are due in connection with this response, the Commissioner is hereby authorized to charge counsel's Deposit Account No. 20-0782/AMAT/2859.C1/KMT, for any fees, including extension of time fees or excess claim fees, required to make this response timely and acceptable to the Office.

**Amendments to the Specification** begin on page 2 of this paper. **Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this paper. **Remarks** begin on page 7 of this paper.

CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR 1.8	
<p>I hereby certify that this correspondence and the documents referred to as attached therein are being facsimile transmitted to the U.S. Patent and Trademark Office to the fax number indicated by the Examiner, namely, fax number 703-872-9306 to the attention of the named Examiner, on the date below.</p> <p><u>4/6/04</u> <u>Rodney G. McDonald</u> Date Signature</p>	

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## FACSIMILE COVER SHEET

**DATE:** April 6, 2004  
**FILE NO.:** AMAT/2859.C1/CPI/COPPER/PJS  
**TO:** Examiner: Rodney G. McDonald  
**FAX NO.:** 703-872-9306  
**COMPANY:** USPTO  
**FROM:** Keith M. Tackett  
**PAGE(S) with cover:** 9  
**ORIGINAL TO FOLLOW?**  YES  NO

### RESPONSE TO FINAL OFFICE ACTION DATED FEBRUARY 6, 2004

**TITLE:** Improved Barrier Layer for Electroplating Processes  
**U.S. SERIAL NO.:** 10/016,255  
**FILING DATE:** December 10, 2001  
**INVENTOR:** Ding, et al.  
**EXAMINER:** Rodney G. McDonald  
**GROUP ART UNIT:** 1753  
**CONFIRMATION NO.:** 7901

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